

BGA2850

MMIC wideband amplifier

Rev. 3 — 26 August 2013

Product data sheet

1. Product profile

1.1 General description

Silicon Monolithic Microwave Integrated Circuit (MMIC) wideband amplifier with internal matching circuit in a 6-pin SOT363 plastic SMD package.

1.2 Features and benefits

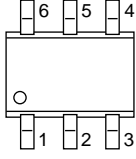
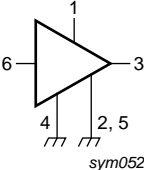
- Internally matched to $50\ \Omega$
- A gain of 24 dB at 950 MHz
- Output power at 1 dB gain compression = $-1\ \text{dBm}$
- Supply current = 9.1 mA at a supply voltage of 5 V
- Reverse isolation > 30 dB up to 2 GHz
- Good linearity with low second order and third order products
- Noise figure = 4.1 dB at 950 MHz
- Unconditionally stable ($K > 1$)

1.3 Applications

- LNB IF amplifiers
- General purpose low noise wideband amplifier for frequencies between DC and 2.2 GHz

2. Pinning information

Table 1. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	V _{CC}		
2, 5	GND2		
3	RF_OUT		
4	GND1		
6	RF_IN		

3. Ordering information

Table 2. Ordering information

Type number	Package		Version
	Name	Description	
BGA2850	-	plastic surface-mounted package; 6 leads	SOT363

4. Marking

Table 3. Marking

Type number	Marking code	Description
BGA2850	*EB	* = - : made in Hong Kong * = p : made in Hong Kong * = W : made in China * = t : made in Malaysia

5. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage	RF input AC coupled	-0.5	+7.0	V
I_{CC}	supply current		-	36	mA
P_{tot}	total power dissipation	$T_{sp} = 90\text{ °C}$	-	200	mW
T_{stg}	storage temperature		-40	+125	°C
T_j	junction temperature		-	125	°C
P_{drive}	drive power		-	-26	dBm

6. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point	$P_{tot} = 200\text{ mW}$; $T_{sp} = 90\text{ °C}$	300	K/W

7. Characteristics

Table 6. Characteristics

$V_{CC} = 5.0\text{ V}$; $Z_S = Z_L = 50\ \Omega$; $P_i = -39\text{ dBm}$; $T_{amb} = 25\text{ °C}$; measured on demo board; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_{CC}	supply voltage		4.5	5.0	5.5	V
I_{CC}	supply current		7.3	9.1	10.8	mA

Table 6. Characteristics ...continued $V_{CC} = 5.0\text{ V}$; $Z_S = Z_L = 50\ \Omega$; $P_i = -39\text{ dBm}$; $T_{amb} = 25\text{ }^\circ\text{C}$; measured on demo board; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G _p	power gain	f = 250 MHz	22.6	23.2	23.8	dB
		f = 950 MHz	23.3	24.0	24.7	dB
		f = 2150 MHz	21.4	22.9	24.3	dB
RL _{in}	input return loss	f = 250 MHz	17	19	21	dB
		f = 950 MHz	15	17	19	dB
		f = 2150 MHz	10	11	18	dB
RL _{out}	output return loss	f = 250 MHz	16	20	25	dB
		f = 950 MHz	11	12	13	dB
		f = 2150 MHz	12	14	17	dB
ISL	isolation	f = 250 MHz	38	58	79	dB
		f = 950 MHz	39	41	43	dB
		f = 2150 MHz	34	37	39	dB
NF	noise figure	f = 250 MHz	3.7	4.2	4.7	dB
		f = 950 MHz	3.7	4.1	4.5	dB
		f = 2150 MHz	3.6	4.0	4.4	dB
B _{-3dB}	-3 dB bandwidth	3 dB below gain at 1 GHz	2.5	2.7	2.9	GHz
K	Rollett stability factor	f = 250 MHz	25	27	29	
		f = 950 MHz	3.1	3.4	3.7	
		f = 2150 MHz	2.0	2.3	2.6	
P _{L(sat)}	saturated output power	f = 250 MHz	1	1	2	dBm
		f = 950 MHz	0	1	2	dBm
		f = 2150 MHz	-2	-1	+1	dBm
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz	-2	-1	-1	dBm
		f = 950 MHz	-2	-1	0	dBm
		f = 2150 MHz	-3	-2	-1	dBm
IP3 _I	input third-order intercept point	P _{drive} = -39 dBm (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	-16	-14	-11	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	-17	-14	-12	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	-18	-15	-12	dBm
IP3 _O	output third-order intercept point	P _{drive} = -39 dBm (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	8	10	12	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz	8	10	12	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz	5	8	11	dBm
P _{L(2H)}	second harmonic output power	P _{drive} = -36 dBm				
		f _{1H} = 250 MHz; f _{2H} = 500 MHz	-65	-63	-61	dBm
		f _{1H} = 950 MHz; f _{2H} = 1900 MHz	-50	-49	-47	dBm
ΔIM2	second-order intermodulation distance	P _{drive} = -39 dBm (for each tone)				
		f ₁ = 250 MHz; f ₂ = 251 MHz	32	43	54	dBc
		f ₁ = 950 MHz; f ₂ = 951 MHz	33	45	56	dBc

8. Application information

[Figure 1](#) shows a typical application circuit for the BGA2850 MMIC. The device is internally matched to 50 Ω, and therefore does not need any external matching. The value of the input and output DC blocking capacitors C2 and C3 should not be more than 100 pF for applications above 100 MHz. However, when the device is operated below 100 MHz, the capacitor value should be increased.

The 22 nF supply decoupling capacitor C1 should be located as close as possible to the MMIC.

The PCB top ground plane, connected to pins 2, 4 and 5 must be as close as possible to the MMIC, preferably also below the MMIC. When using via holes, use multiple via holes as close as possible to the MMIC.

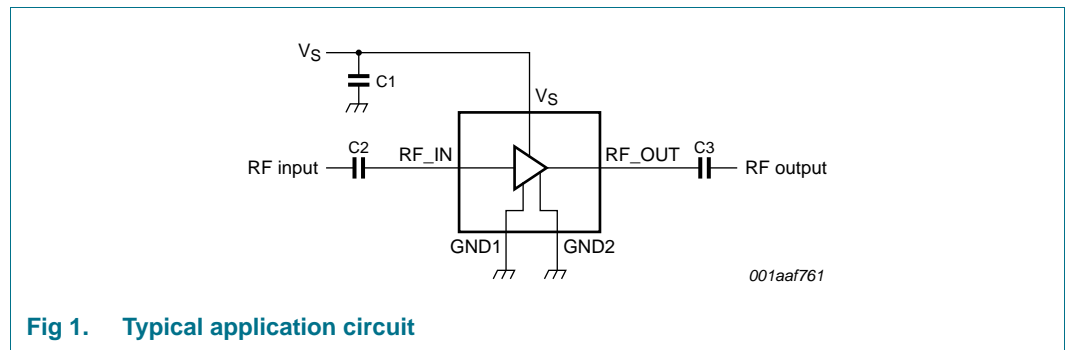
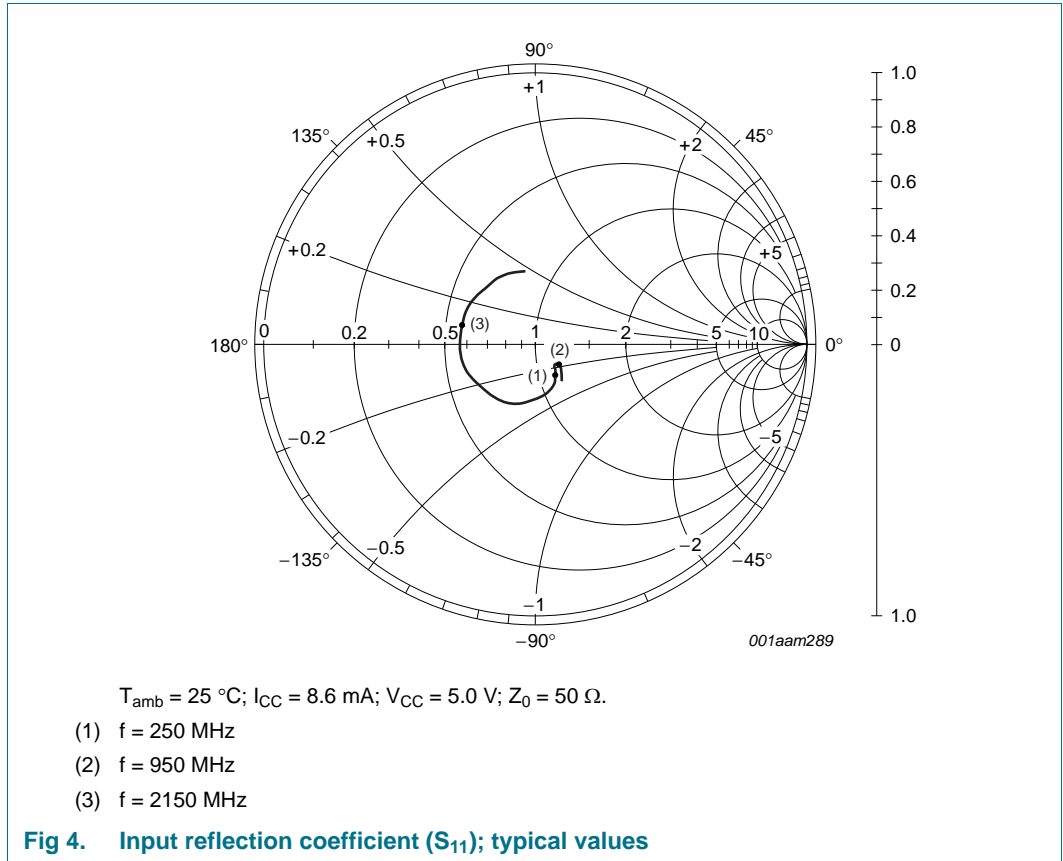


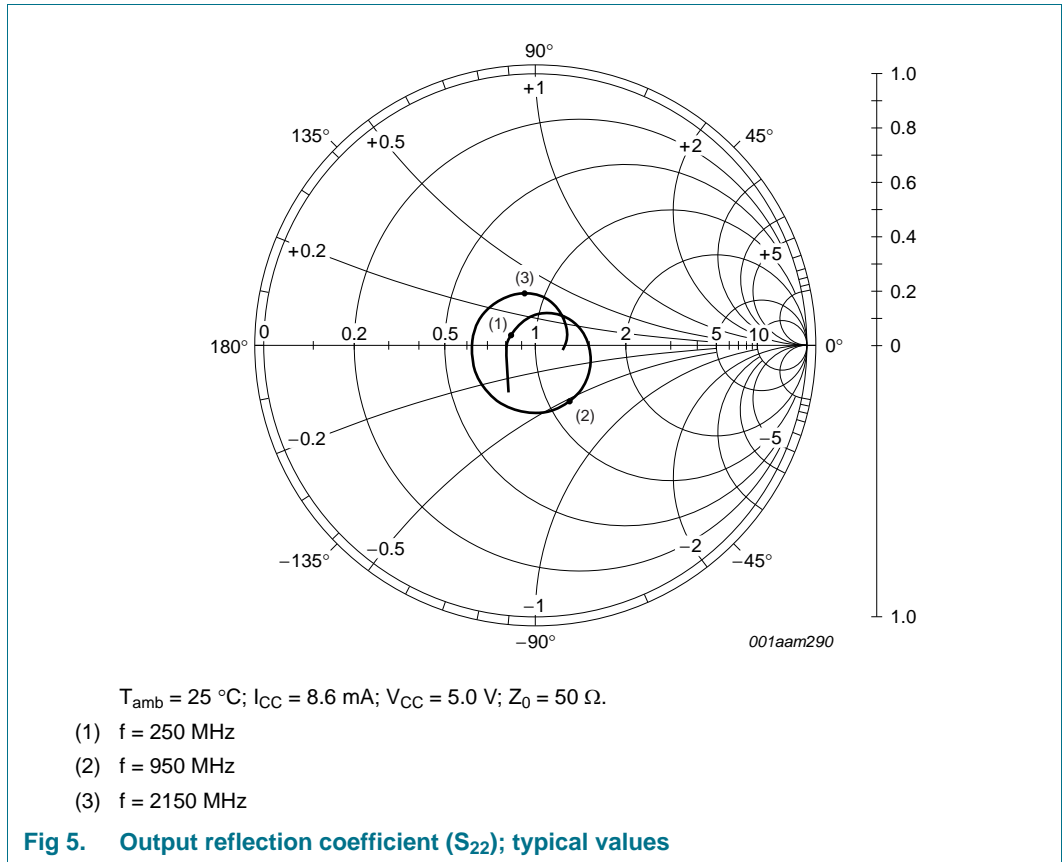
Fig 1. Typical application circuit

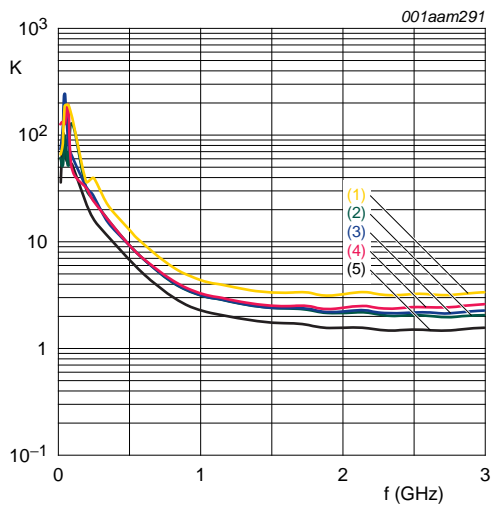
8.1 Application examples

<p style="text-align: right;">001aaf762</p> <p>The MMIC is very suitable as IF amplifier in e.g. LNB's. The excellent wideband characteristics make it an easy building block.</p> <p>Fig 2. Application as IF amplifier</p>	<p style="text-align: right;">001aaf763</p> <p>As second amplifier after an LNA, the MMIC offers an easy matching, low noise solution.</p> <p>Fig 3. Application as RF amplifier</p>
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8.2 Graphs

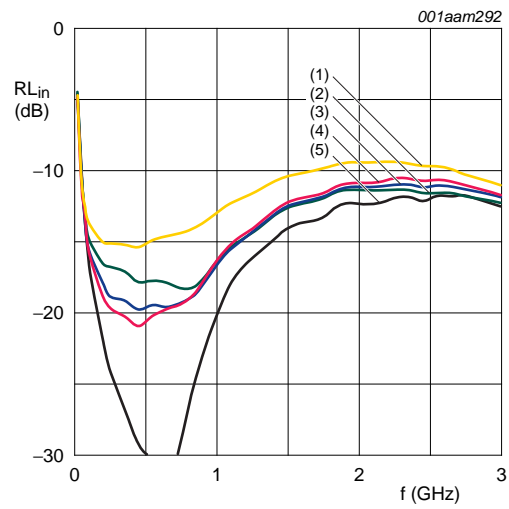






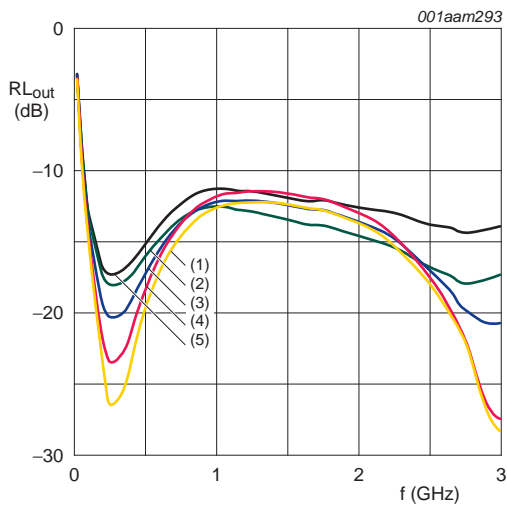
- $P_{drive} = -40 \text{ dBm}; Z_0 = 50 \Omega.$
- (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 - (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 - (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 - (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 - (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 6. Rollett stability factor as function of frequency; typical values



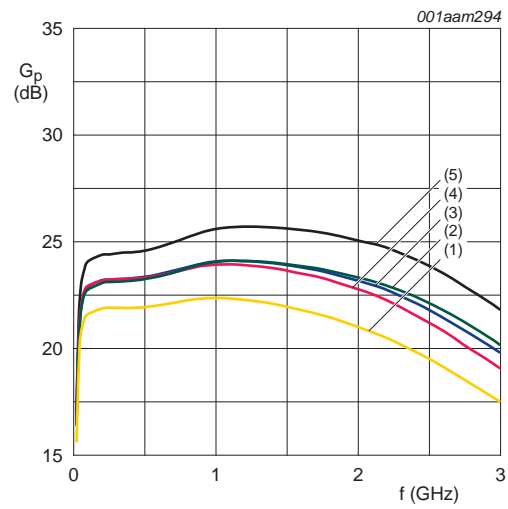
- $P_{drive} = -40 \text{ dBm}; Z_0 = 50 \Omega.$
- (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 - (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 - (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 - (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 - (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 7. Input return loss as function of frequency; typical values



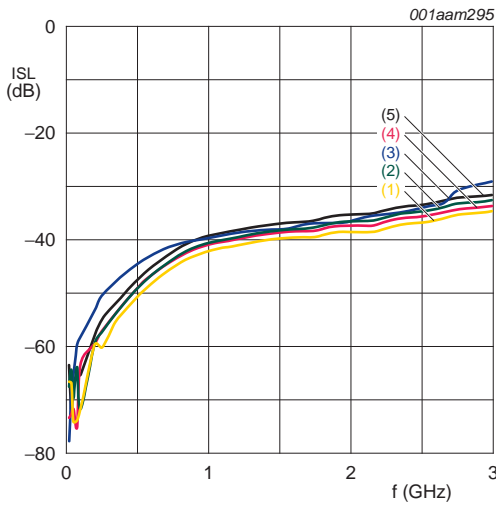
- $P_{drive} = -40 \text{ dBm}; Z_0 = 50 \Omega.$
- (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 - (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 - (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 - (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 - (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 8. Output return loss as function of frequency; typical values



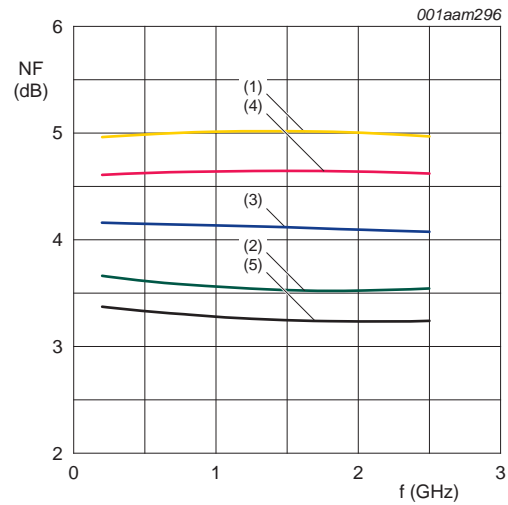
- $P_{drive} = -40 \text{ dBm}; Z_0 = 50 \Omega.$
- (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 - (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 - (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 - (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 - (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 9. Power gain as function of frequency; typical values



$P_{drive} = -40 \text{ dBm}; Z_0 = 50 \Omega.$
 (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 10. Isolation as function of frequency; typical values



$Z_0 = 50 \Omega.$
 (1) $V_{CC} = 4.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 7.13 \text{ mA}$
 (2) $V_{CC} = 4.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 8.50 \text{ mA}$
 (3) $V_{CC} = 5.0 \text{ V}; T_{amb} = 25 \text{ }^\circ\text{C}; I_{CC} = 8.65 \text{ mA}$
 (4) $V_{CC} = 5.5 \text{ V}; T_{amb} = 85 \text{ }^\circ\text{C}; I_{CC} = 8.85 \text{ mA}$
 (5) $V_{CC} = 5.5 \text{ V}; T_{amb} = -40 \text{ }^\circ\text{C}; I_{CC} = 10.28 \text{ mA}$

Fig 11. Noise figure as function of frequency; typical values

8.3 Tables

Table 7. Supply current over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	$T_{amb} \text{ (}^\circ\text{C)}$			Unit
			-40	+25	+85	
I_{CC}	supply current	$V_{CC} = 4.5 \text{ V}$	8.50	7.74	7.13	mA
		$V_{CC} = 5.0 \text{ V}$	9.40	8.65	7.99	mA
		$V_{CC} = 5.5 \text{ V}$	10.28	9.52	8.85	mA

Table 8. Second harmonic output power over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	$T_{amb} \text{ (}^\circ\text{C)}$			Unit	
			-40	+25	+85		
$P_{L(2H)}$	second harmonic output power	$f = 250 \text{ MHz}; P_{drive} = -36 \text{ dBm}$	$V_{CC} = 4.5 \text{ V}$	-55	-62	-61	dBm
			$V_{CC} = 5.0 \text{ V}$	-56	-63	-60	dBm
			$V_{CC} = 5.5 \text{ V}$	-58	-63	-61	dBm
		$f = 950 \text{ MHz}; P_{drive} = -36 \text{ dBm}$	$V_{CC} = 4.5 \text{ V}$	-46	-50	-54	dBm
			$V_{CC} = 5.0 \text{ V}$	-46	-49	-52	dBm
			$V_{CC} = 5.5 \text{ V}$	-45	-48	-51	dBm

Table 9. Input power at 1 dB gain compression over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
P _{i(1dB)}	input power at 1 dB gain compression	f = 250 MHz				
		V _{CC} = 4.5 V	-23.7	-23.7	-23.5	dBm
		V _{CC} = 5.0 V	-23.4	-23.4	-23.3	dBm
		V _{CC} = 5.5 V	-23.0	-23.1	-23.1	dBm
		f = 950 MHz				
		V _{CC} = 4.5 V	-24.1	-23.9	-23.7	dBm
		V _{CC} = 5.0 V	-23.8	-23.7	-23.6	dBm
		V _{CC} = 5.5 V	-23.5	-23.5	-23.4	dBm
		f = 2150 MHz				
		V _{CC} = 4.5 V	-24.2	-24.0	-23.9	dBm
		V _{CC} = 5.0 V	-24.0	-23.9	-23.9	dBm
		V _{CC} = 5.5 V	-23.8	-23.8	-24.0	dBm

Table 10. Output power at 1 dB gain compression over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
P _{L(1dB)}	output power at 1 dB gain compression	f = 250 MHz				
		V _{CC} = 4.5 V	-1.8	-2.4	-2.9	dBm
		V _{CC} = 5.0 V	-0.7	-1.4	-1.9	dBm
		V _{CC} = 5.5 V	+0.3	-0.5	-1.1	dBm
		f = 950 MHz				
		V _{CC} = 4.5 V	-1.6	-2.3	-3.0	dBm
		V _{CC} = 5.0 V	-0.5	-1.2	-1.9	dBm
		V _{CC} = 5.5 V	+0.6	-0.3	-1.1	dBm
		f = 2150 MHz				
		V _{CC} = 4.5 V	-2.4	-3.4	-4.5	dBm
		V _{CC} = 5.0 V	-1.2	-2.3	-3.5	dBm
		V _{CC} = 5.5 V	-0.1	-1.5	-2.8	dBm

Table 11. Saturated output power over temperature and supply voltages

Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
P _{L(sat)}	saturated output power	f = 250 MHz				
		V _{CC} = 4.5 V	+0.2	-0.3	-0.8	dBm
		V _{CC} = 5.0 V	1.9	1.4	0.9	dBm
		V _{CC} = 5.5 V	2.8	2.1	1.6	dBm
		f = 950 MHz				
		V _{CC} = 4.5 V	+0.6	-0.1	-0.8	dBm
		V _{CC} = 5.0 V	1.6	0.9	0.2	dBm
		V _{CC} = 5.5 V	3.2	1.7	1.7	dBm
		f = 2150 MHz				
		V _{CC} = 4.5 V	-0.5	-1.5	-2.7	dBm
		V _{CC} = 5.0 V	+0.5	-0.5	-1.8	dBm
		V _{CC} = 5.5 V	+1.5	+0.2	-1.1	dBm

Table 12. Second-order intermodulation distance over temperature and supply voltages

Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
ΔIM2	second-order intermodulation distance	f ₁ = 250 MHz; f ₂ = 251 MHz; P _{drive} = -39 dBm				
		V _{CC} = 4.5 V	50	44	37	dBc
		V _{CC} = 5.0 V	57	43	37	dBc
		V _{CC} = 5.5 V	51	42	37	dBc
		f ₁ = 950 MHz; f ₂ = 951 MHz; P _{drive} = -39 dBm				
		V _{CC} = 4.5 V	45	47	37	dBc
		V _{CC} = 5.0 V	55	44	37	dBc
		V _{CC} = 5.5 V	60	43	37	dBc

Table 13. Output third-order intercept point over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit	
			-40	+25	+85		
IP _{3O}	output third-order intercept point	f ₁ = 250 MHz; f ₂ = 251 MHz; P _{drive} = -39 dBm	V _{CC} = 4.5 V	9.9	8.3	6.9	dBm
			V _{CC} = 5.0 V	11.5	9.7	8.2	dBm
			V _{CC} = 5.5 V	12.7	10.6	9.3	dBm
		f ₁ = 950 MHz; f ₂ = 951 MHz; P _{drive} = -39 dBm	V _{CC} = 4.5 V	10.0	8.4	6.9	dBm
			V _{CC} = 5.0 V	11.4	9.7	8.1	dBm
			V _{CC} = 5.5 V	12.7	10.7	9.1	dBm
		f ₁ = 2150 MHz; f ₂ = 2151 MHz; P _{drive} = -39 dBm	V _{CC} = 4.5 V	8.1	6.6	4.7	dBm
			V _{CC} = 5.0 V	9.6	7.7	6.0	dBm
			V _{CC} = 5.5 V	10.6	8.6	6.5	dBm

Table 14. -3 dB bandwidth over temperature and supply voltages
Typical values.

Symbol	Parameter	Conditions	T _{amb} (°C)			Unit
			-40	+25	+85	
B _{-3dB}	-3 dB bandwidth	V _{CC} = 4.5 V	2.798	2.670	2.539	GHz
		V _{CC} = 5.0 V	2.814	2.690	2.554	GHz
		V _{CC} = 5.5 V	2.826	2.701	2.570	GHz

9. Test information

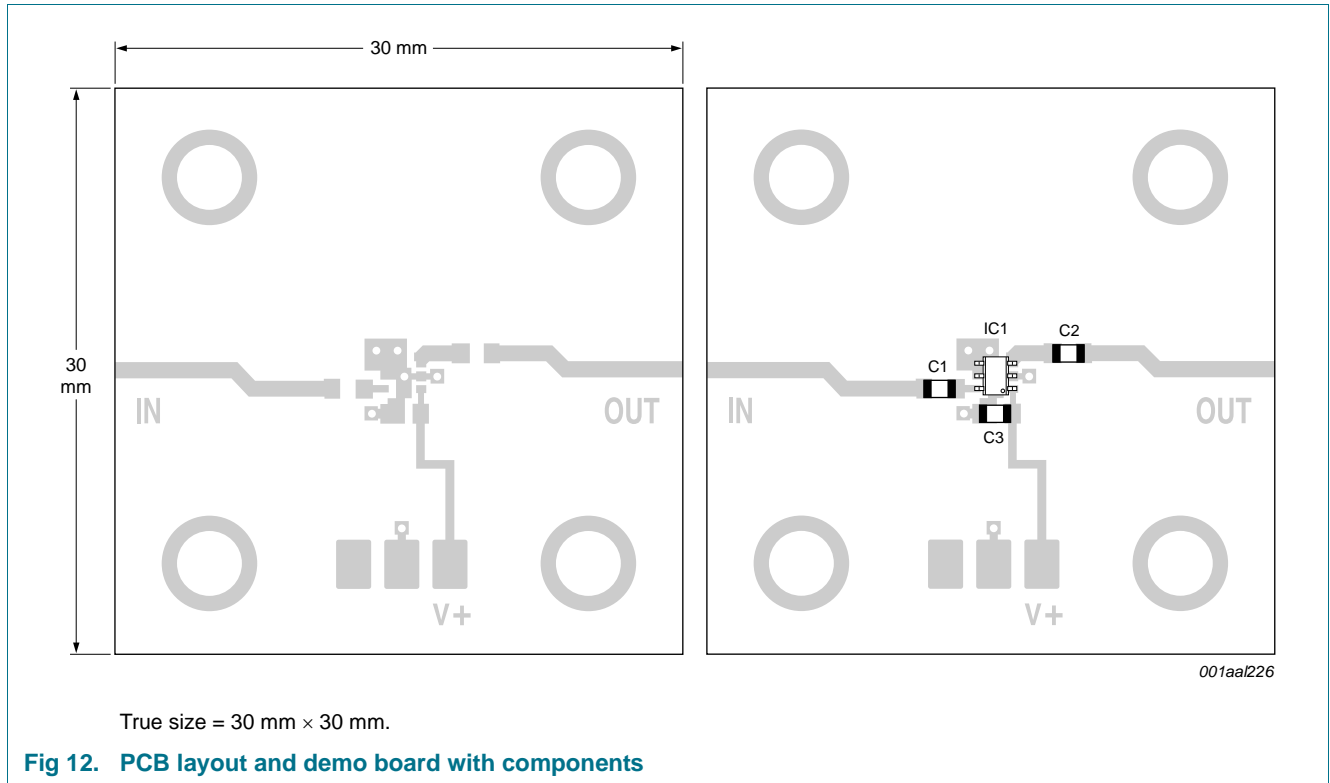


Table 15. List of components used for the typical application

Component	Description	Value	Dimensions
C1, C2	multilayer ceramic chip capacitor	100 pF	0603
C3	multilayer ceramic chip capacitor	22 nF	0603
IC1	BGA2850 MMIC	-	SOT363

10. Package outline

Plastic surface-mounted package; 6 leads

SOT363

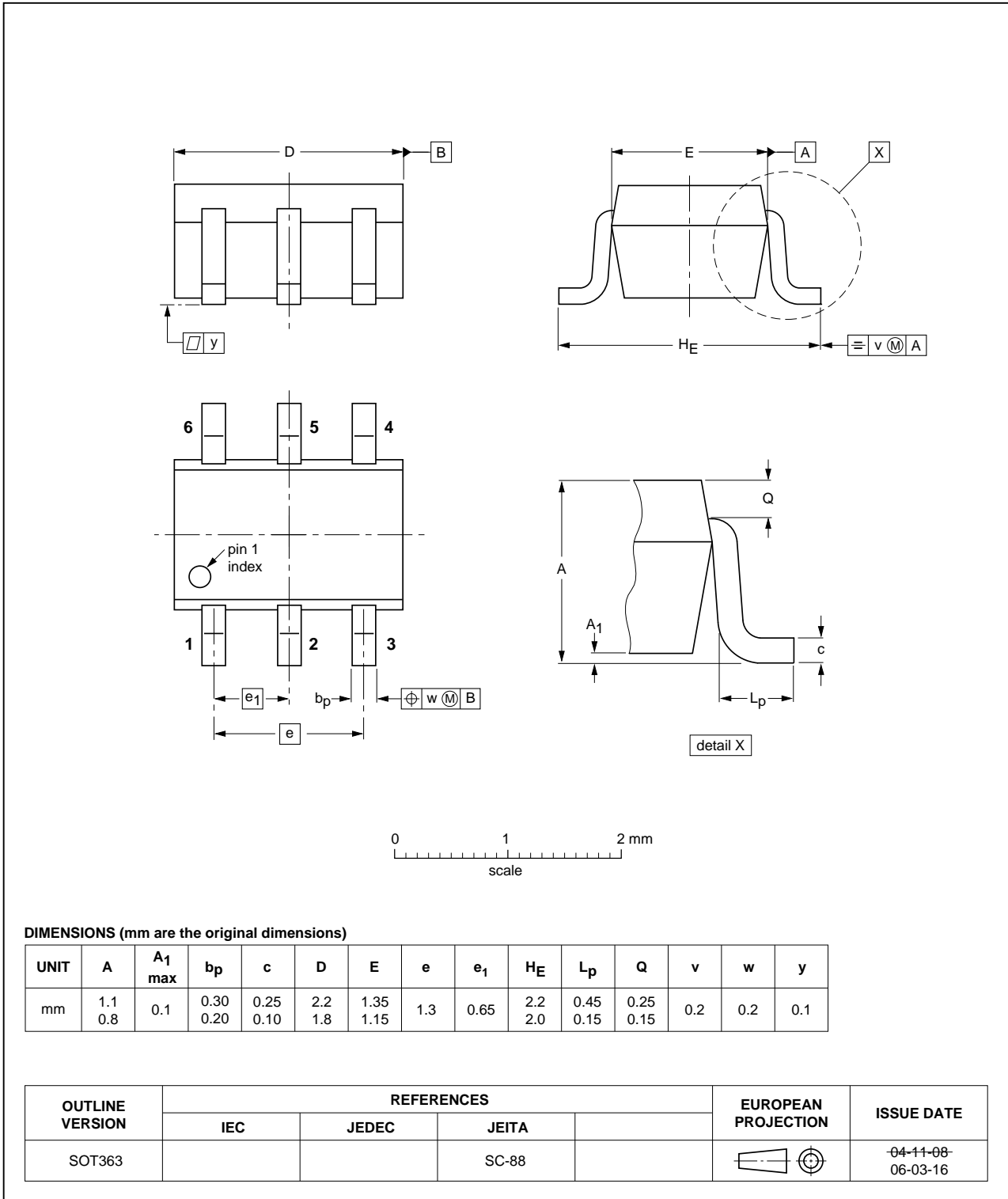


Fig 13. Package outline SOT363

11. Abbreviations

Table 16. Abbreviations

Acronym	Description
IF	Intermediate Frequency
LNA	Low-Noise Amplifier
LNB	Low-Noise Block converter
PCB	Printed-Circuit Board
SMD	Surface Mounted Device

12. Revision history

Table 17. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BGA2850 v.3	20130826	Product data sheet	-	BGA2850 v.2
Modifications	<ul style="list-style-type: none">Table 4 on page 2: the maximum value for V_{CC} has been changed to 7 V			
BGA2850 v.2	20101029	Product data sheet	-	BGA2850 v.1
BGA2850 v.1	20100817	Product data sheet	-	-

13. Legal information

13.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

13.2 Definitions

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